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PATENT



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Chi-Hui LIN, Chung-Lin HUANG

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10/725,052

Examiner:

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PRENTY, MARK V

Conf. No.:

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Art Unit: 2822

Title: STACKED GATE FLASH MEMORY DEVICE AND METHOD OF

**FABRICATING THE SAME** 

Date: April 19, 2004

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Respectfully submitted,

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